

AMENDMENTS TO THE SPECIFICATION

In the Specification

Please substitute the following amended paragraph(s) and/or section(s) (deleted matter is shown by strikethrough and added matter is shown by underlining):

Page 8, line 9-line page 9, line 16:

The present invention may be implemented at least in part as computer software on a conventional computer system. Referring now to Figure 1, a conventional computer system 150 for practicing the present invention is shown. Processor 160 retrieves and executes software instructions stored in storage 162 such as memory, which may be Random Access Memory (RAM) and may control other components to perform the present invention. Storage 162 may be used to store program instructions or data or both. Storage 164, such as a computer disk drive or other nonvolatile storage, may provide storage of data or program instructions. In one embodiment, storage 164 provides longer term storage of instructions and data, with storage 162 providing storage for data or instructions that may only be required for a shorter time than that of storage 164. Input device 166 such as a computer keyboard or mouse or both allows user input to the system 150. Output 168, such as a display or printer, allows the system to provide information such as instructions, data or other information to the user of the system 150. Storage input device 170 such as a conventional floppy disk drive or CD-ROM drive accepts via input 172 computer program products 174 such as a conventional floppy disk or CD-ROM or other nonvolatile storage media that may be used to transport computer instructions or data to the system 150. Computer program product 174 has encoded thereon computer readable program

code devices ~~[[176]]~~ 178, such as magnetic charges in the case of a floppy disk or optical encodings in the case of a CD-ROM which are encoded as program instructions, data or both to configure the computer system 150 to operate as described below.

Page 25, line 20 - page 27, line 2:

To generate output to an external system, processor B 232 and processor C 234 write any response to the block to their respective private memory interface 250 or 260, each providing a conventional memory interface to the private memory bus of processor B 232 and processor C 234, respectively, of front side bus 220. Private memory interfaces 250, 260 each pass the response received to a location in a buffer at the tail of outgoing storage B and C 252, 262, respectively. Each outgoing storage 252, 262 is a conventional storage device, such as memory or disk storage, arranged as a conventional double buffer, with a head and a tail as described above with respect to FIFOs 286, 288. As will be described below, it is not necessary to utilize separate outgoing storages 252, 262 as a single outgoing storage can be used, with each private memory interface 250, 260 writing into the single outgoing storage 252 or 262, although it can simplify the circuitry to have individual outgoing 252, 262 to avoid contention issues. The responses are written in blocks having a fixed or variable length as described above. In one embodiment, each processor ~~234, 262~~ 232, 234 writes to a single memory location, and the respective private memory interface 250, 260 manages placing it into a buffer in the respective outgoing storage 252, 262 and updating the respective tail pointer. In another embodiment, each processor 232, 234 reads the tail in the respective outgoing storage 252, 262 and stores the response or other communication into the private memory buffer location in the respective

outgoing storage 252, 262 indicated by the tail pointer in the respective outgoing storage 252, 262, and updates the tail pointer.